Guidelines for FPGA Session Demonstration at A-SSCC 2018

Background

A-SSCC honorably introduced FPGA Session from 2018 in order to align the rapid speed of the industry growth and the diversity of the applications to show the proof of concept by FPGA implementation with quick turnaround and cheaper cost than the real SoC chip designs and Si fabrications.

Information

In FPGA Session, FPGA Demonstration is mandatory to show the proof of concepts.

General Guidelines

The FPGA Session Demonstration is intended to provide an interactive demonstration to show their proof of concept. Authors can demonstrate their design results, operations and detailed concepts vividly with FPGA to the conference participants.
Authors should bring their FPGA platform and test/demonstration equipment to the conference site to demonstrate their design results during the conference period with SDC (Student Design Contest); from the first day till the second day of the conference (The first day (Nov. 5) demonstration is very important because the award winners are determined by the demonstration review on the first day).

Demonstration Schedule:

- Monday, November 5 17:00-19:00 (Installation is available from 14:00)
- Tuesday, November 6 10:50-16:00

The operation of the FPGA platform will be demonstrated by showing the important waveforms verifying the design concepts or by showing visual criterions for the performance of the chip.

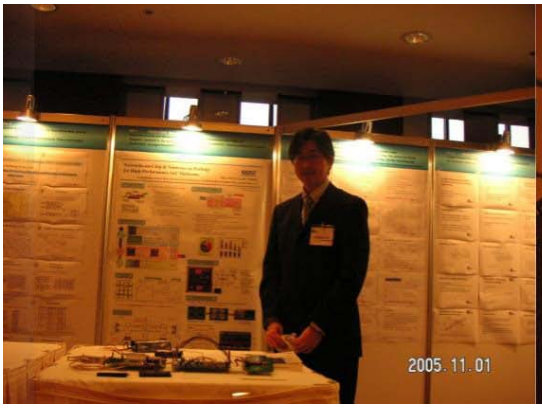
Most of the test equipment necessary for the demonstration should be brought by the authors themselves (The notebook type test equipment is recommended). However, if you need any special equipment for the on-site demonstration, please contact the FPGA Session chair, Dr. Shigeki Tomishima (shigeki.tomishima@intel.com) at your earliest convenience.

*Presenters are responsible to pay off special equipment fee prior to the conference. Please contact the A-SSCC Secretariat at pobsadue@poetry-life.com for detail payment information.

The poster boards are prepared to attach the posters in portrait format with a maximum size of 2.1m (height) by 0.9m (width). Posters have to be attached to the boards using adhesive tape (available on site). The poster should allow the attendees to recognize the key points of the work easily from a distance of at least 10 feet (3 meters) and to facilitate more detailed discussions with the authors. Often the best poster comprises a few large significant graphs/illustrations annotated clearly with only enough text to headline key points like purpose, conclusions, and impact. Note that the poster should not consist of a copy of the manuscript.

Specific Guidelines

- A 1.8m X 0.6m X 0.7m (Length X Width X Height) platform will be placed in front of the each poster board for the design demonstration. Power socket and wireless internet will also be provided.
- Please prepare a self-complete test board in order to avoid the necessity of the external test equipment such as the function generator or large oscilloscope.
- An area 0.3m high and 0.21m (A4 size) wide in the top left corner of the poster board will be used for poster identification.
- The poster will typically consist of a number of 0.21m x 0.3m or A4 pages tacked to the bulletin board, although larger paper formats are acceptable and even desirable.
- One page is to consist of the paper title, authors and affiliation.
- A second page is to consist of a brief abstract outlining the key points of the paper.
- Use a minimum font size of 18 points for text and 30 points for headings.
- Limit text to 4-6 key points per page.
- Limit graphs or illustrations to 2 per page.
- Appropriate use of color is encouraged.



(An example of Demo and Poster)

(An example of the demo kit)